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FACSIMILE TRANSMISSION

PLEASE DELIVER THE FOLLOWING PAGES

TO : United States Patent and Trademark Office
ATTN.: Examiner Thao X. Le Group Art Unit 2814
FAX NO.: (571) 273-8300
FROM : Paul Teng OUR DOCKET NO.: 2271/76704
DATE : October 16, 2007

TOTAL NUMBER OF PAGES, INCLUDING COVER SHEET: 3

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❖ MESSAGE ❖

In connection with Serial No. 10/588,479:

Communication In Response To September 27, 2007 Restriction
Requiremen

Due Date: October 27, 2007

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Dkt. 2271/76704

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of: Masaaki YOSHIDA

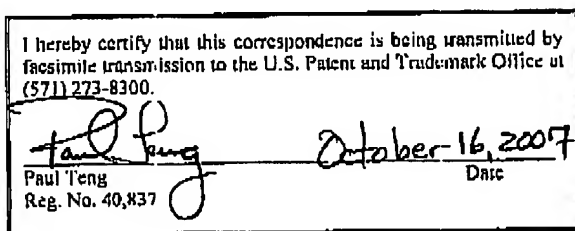
Serial No.: 10/588,479

Group Art Unit 2814

Date Filed: August 1, 2006

Examiner: Thao X. Le

For: SEMICONDUCTOR DEVICE



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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

**COMMUNICATION IN RESPONSE TO
SEPTEMBER 27, 2007 OFFICE ACTION**

This Communication is submitted in response to the September 27, 2007 Office Action issued by the U.S. Patent and Trademark Office which indicates that examination of the above-identified patent application will be restricted to one of the following allegedly distinct species:

- Species A. Claims 1-8 directed to a memory device wherein the memory gate oxide is arranged to be thinner than the peripheral circuit gate oxide film;
- Species B. Claims 9-10 directed to a voltage detecting circuit having a divider resistor circuit that divides an input, output, divided, and reference voltages wherein the peripheral circuit gate oxide film is thicker than the memory gate oxide film;
- Species C. Claim 11 directed to a constant voltage generating circuit having output driver wherein the peripheral circuit gate oxide film is thicker than the memory gate oxide film;
- Species D. Claims 12-19 directed to a memory device where the impurity concentration within the polysilicon of the floating gate is lower than an impurity concentration within the polysilicon of the peripheral circuit gate oxide film;

Masaaki YOSHIDA, S.N. 10/588,479

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Species E. Claims 20-21 directed to a voltage detecting circuit having a divider resistor circuit that divides an input, output, divided, and references voltages wherein the peripheral gate has higher impurity concentration than the polysilicon of the floating gate; and

Species F. Claim 22 directed to a constant voltage generating circuit having output driver wherein the peripheral gate has higher impurity concentration than the polysilicon of the floating gate.

Applicant hereby elects, without traverse, to prosecute Species A. Pending claims readable on Species A include claims 1-8.

If a petition for an extension of time is required to make this response timely, this paper should be considered to be such a petition. The Office is hereby authorized to charge any fees that may be required in connection with this response and to credit any overpayment to our Deposit Account No. 03-3125.

If a telephone interview could advance the prosecution of this application, the Examiner is respectfully requested to call the undersigned attorney.

Respectfully submitted,



Paul Teng, Reg. No. 40,837
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